

FIGURE 1

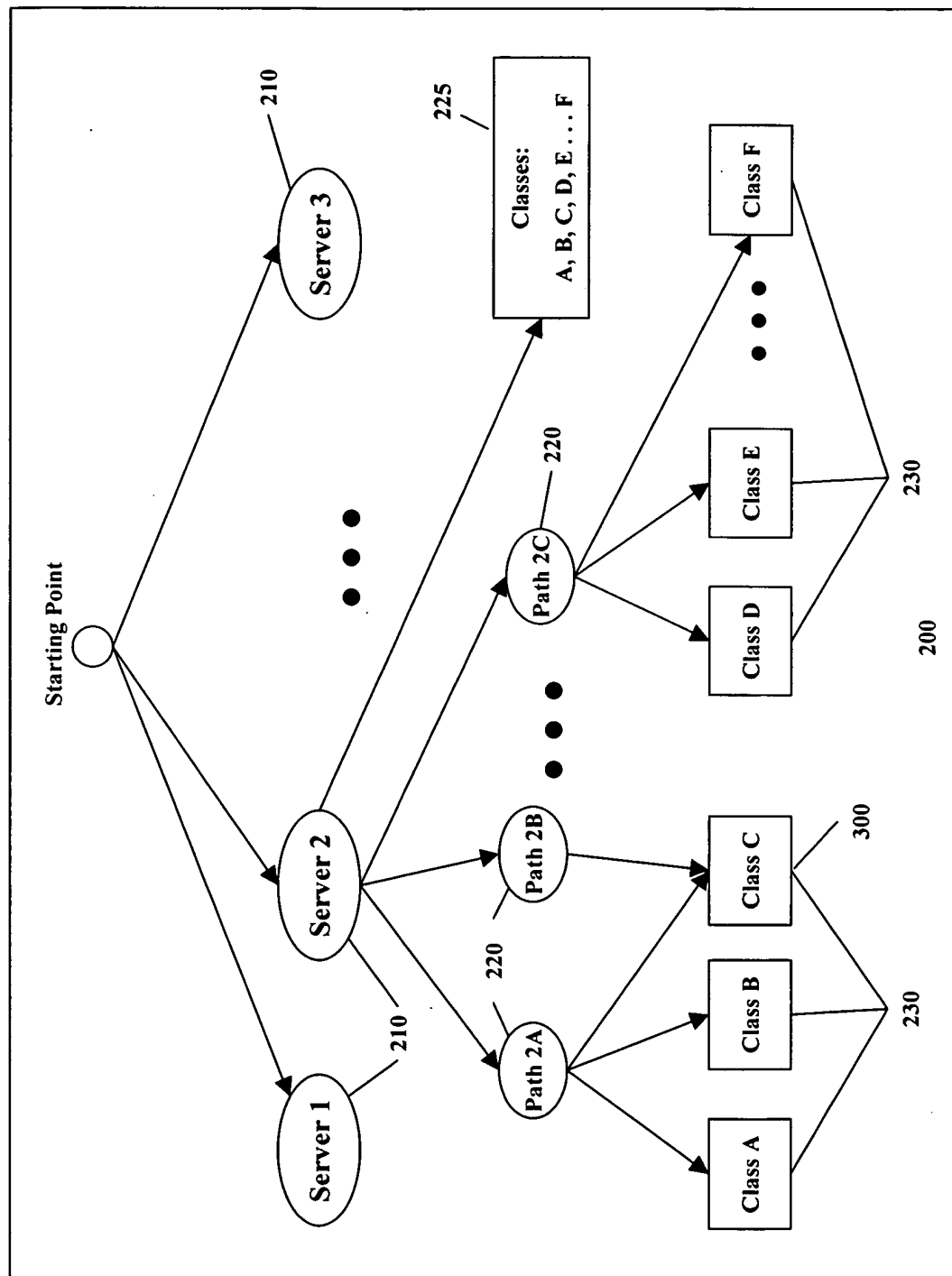


FIGURE 2

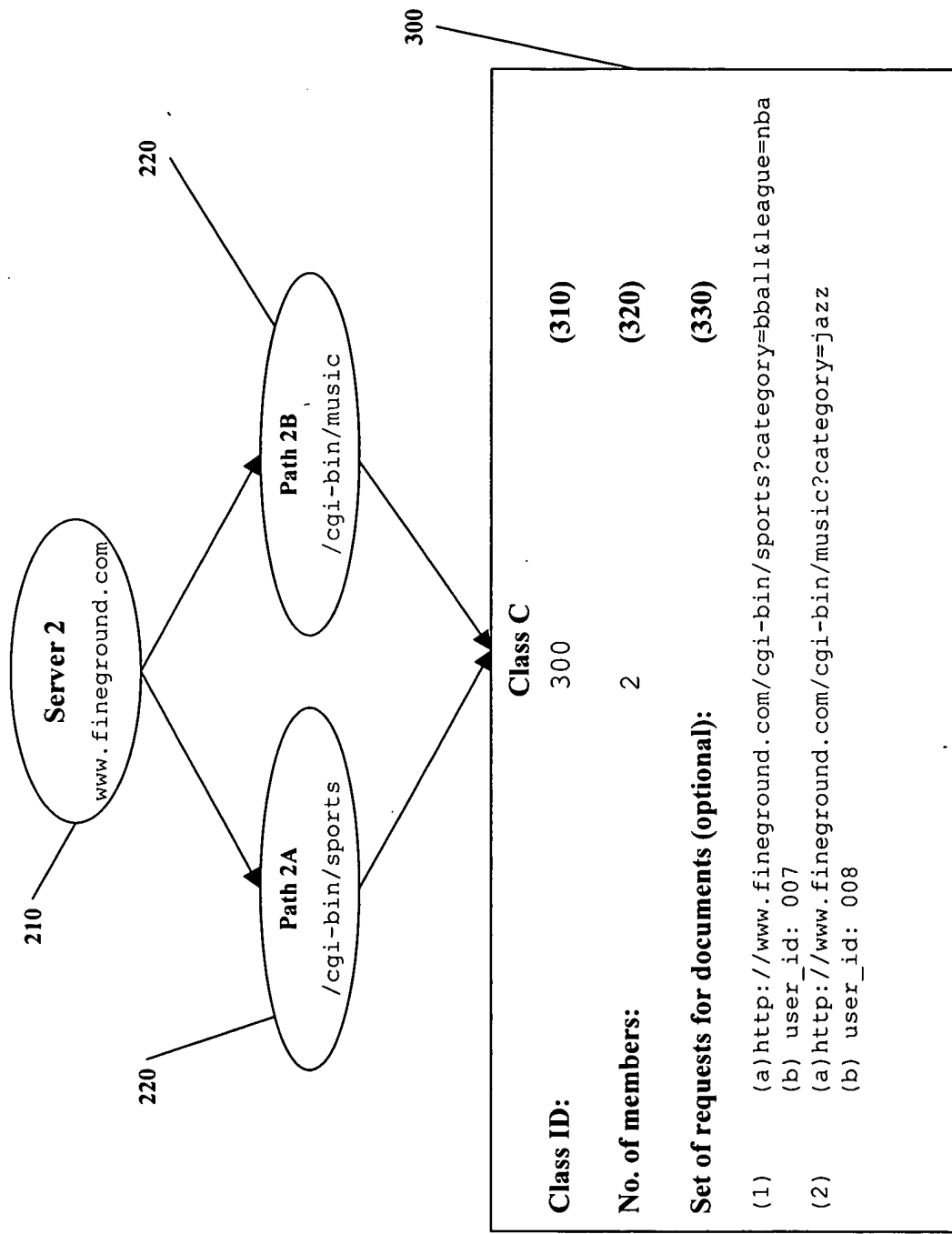


FIGURE 3

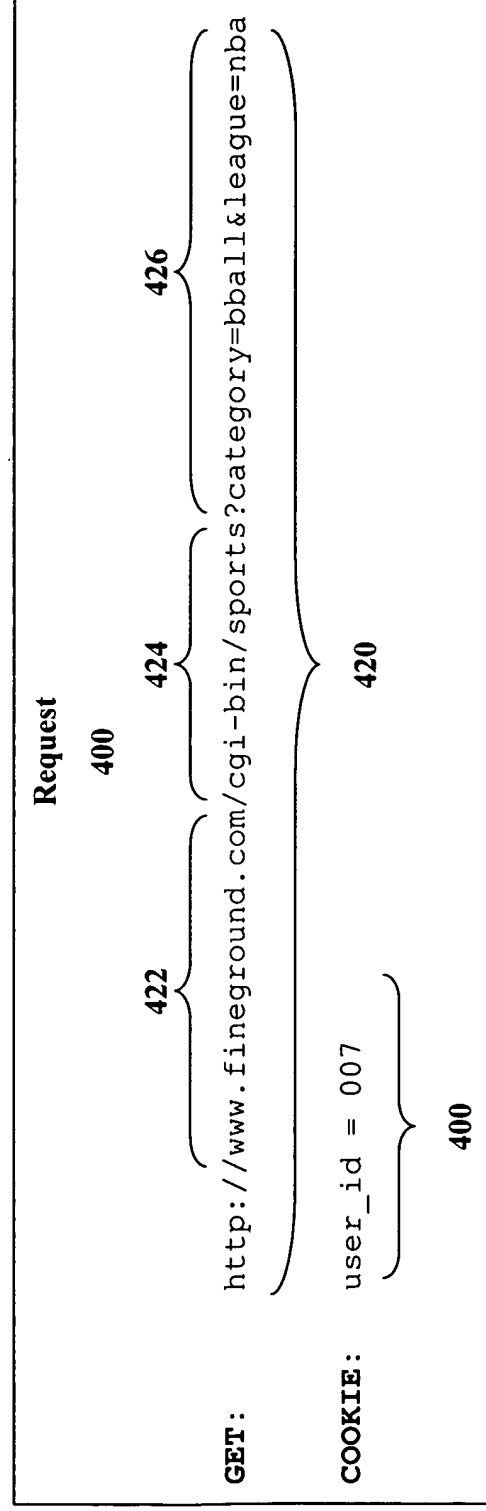


FIGURE 4

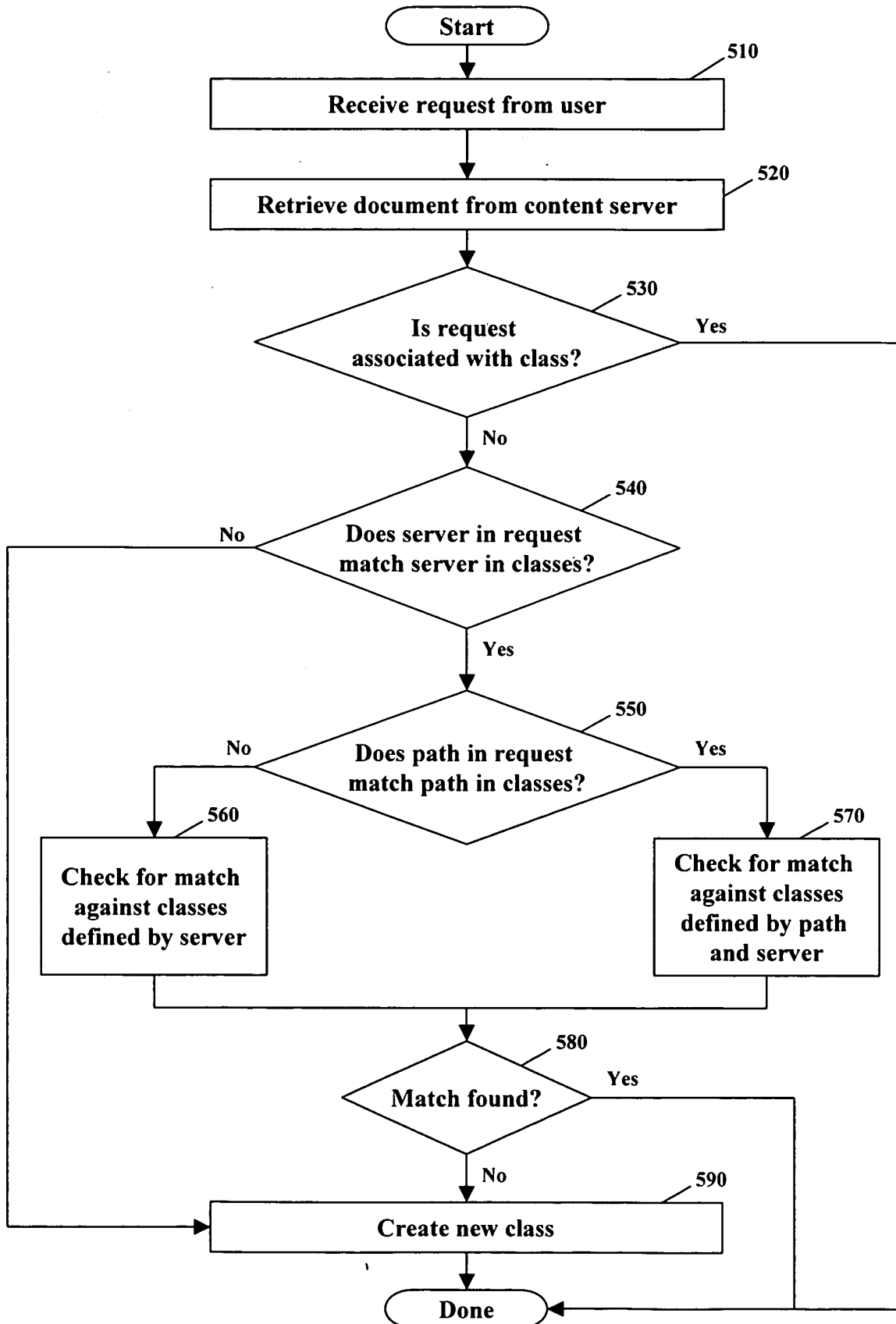


FIGURE 5

00016003.032204

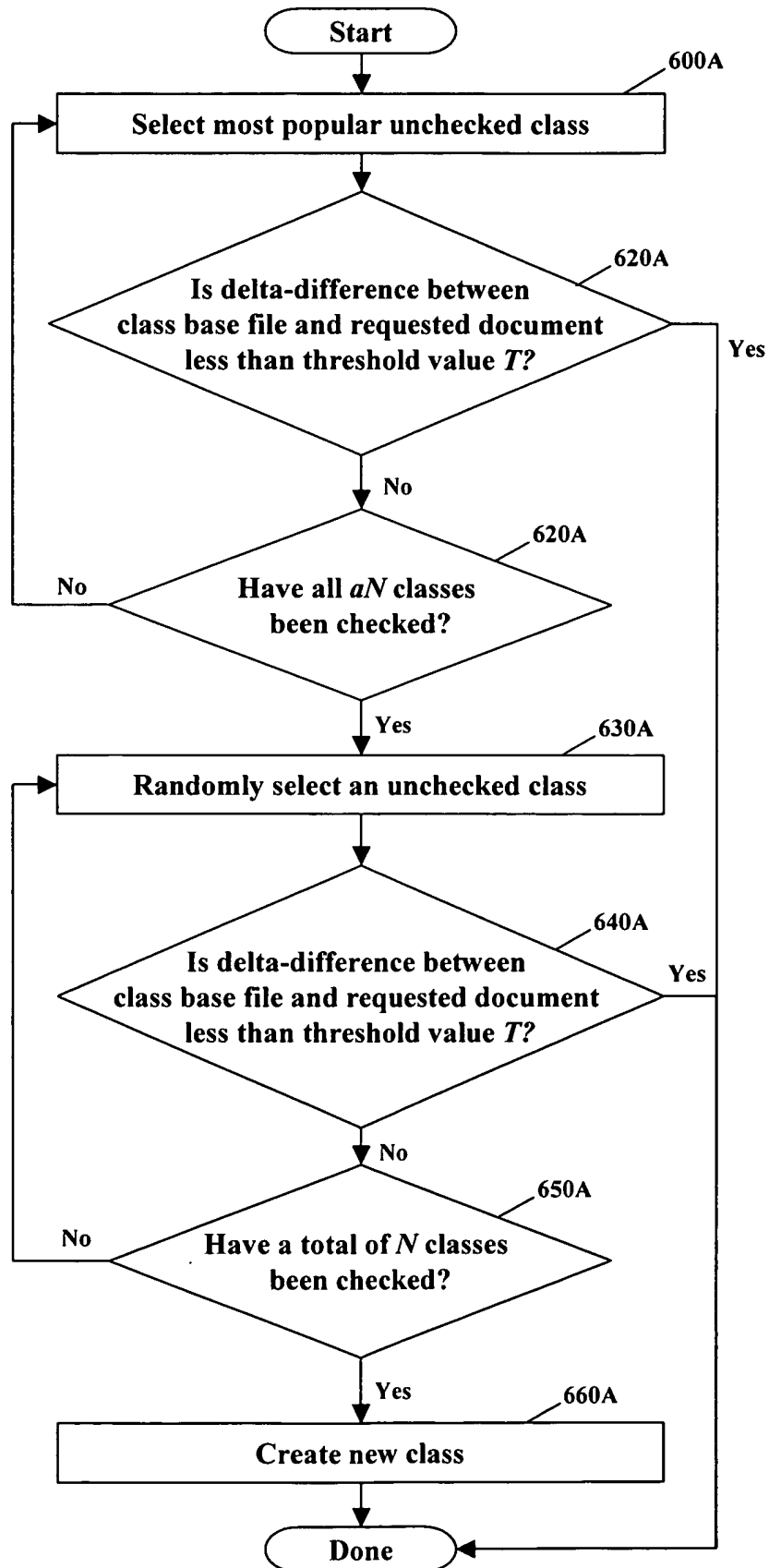


FIGURE 6A

00016802.03204
FIG. 6B

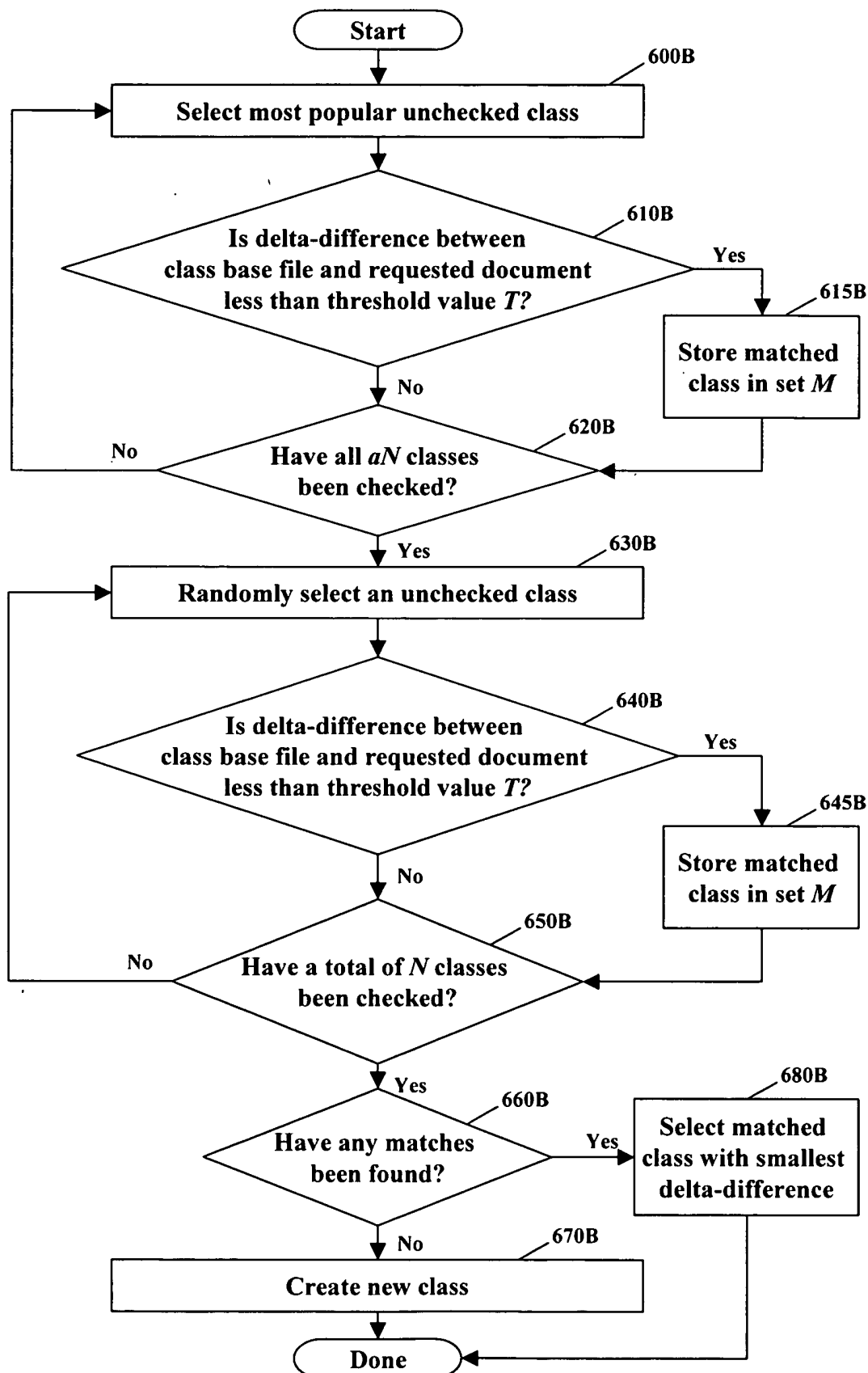


FIGURE 6B

00016001-032201
000000-000000

	Sample 1	Sample 2	Sample 3		Sample K
Sample 1	-	d_{21}	d_{31}		d_{K1}
Sample 2	d_{12}	-	d_{32}		d_{K2}
Sample 3	d_{13}	d_{23}	-		d_{K3}
=====					
Sample K	d_{1K}	d_{2K}	d_{3K}		-
=====					
Sums	Σd_{1i}	Σd_{2i}	Σd_{3i}		Σd_{Ki}

700

FIGURE 7

7-10-68

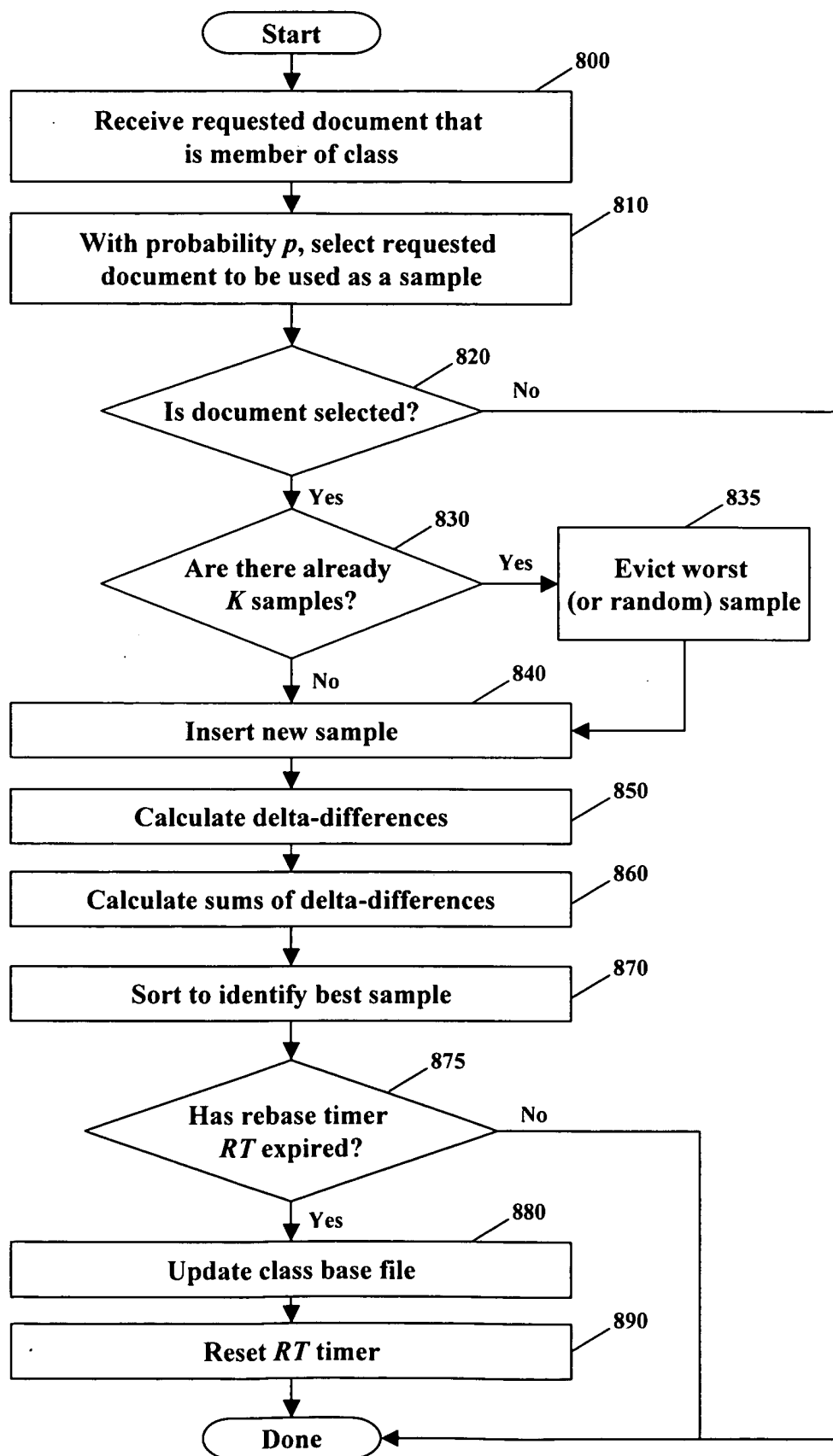


FIGURE 8

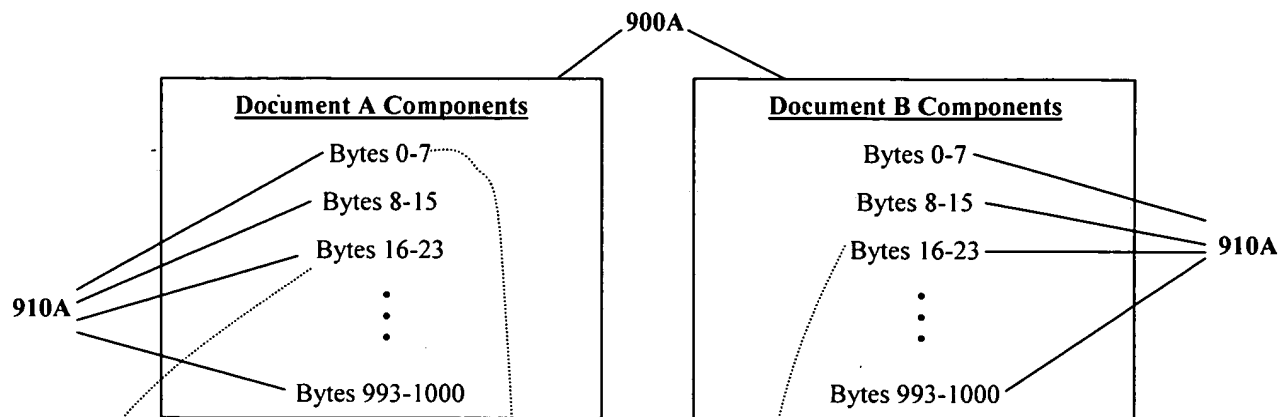


FIGURE 9A

COMPONENTS	COUNTER	ACTIVE/INACTIVE BIT
Component A	12	Active
Component B	15	Active
Component C	10	Active
⋮	⋮	⋮
Component Y	1	Inactive
Component Z	0	Inactive

FIGURE 9B

00015000 03204
00000000 00000000

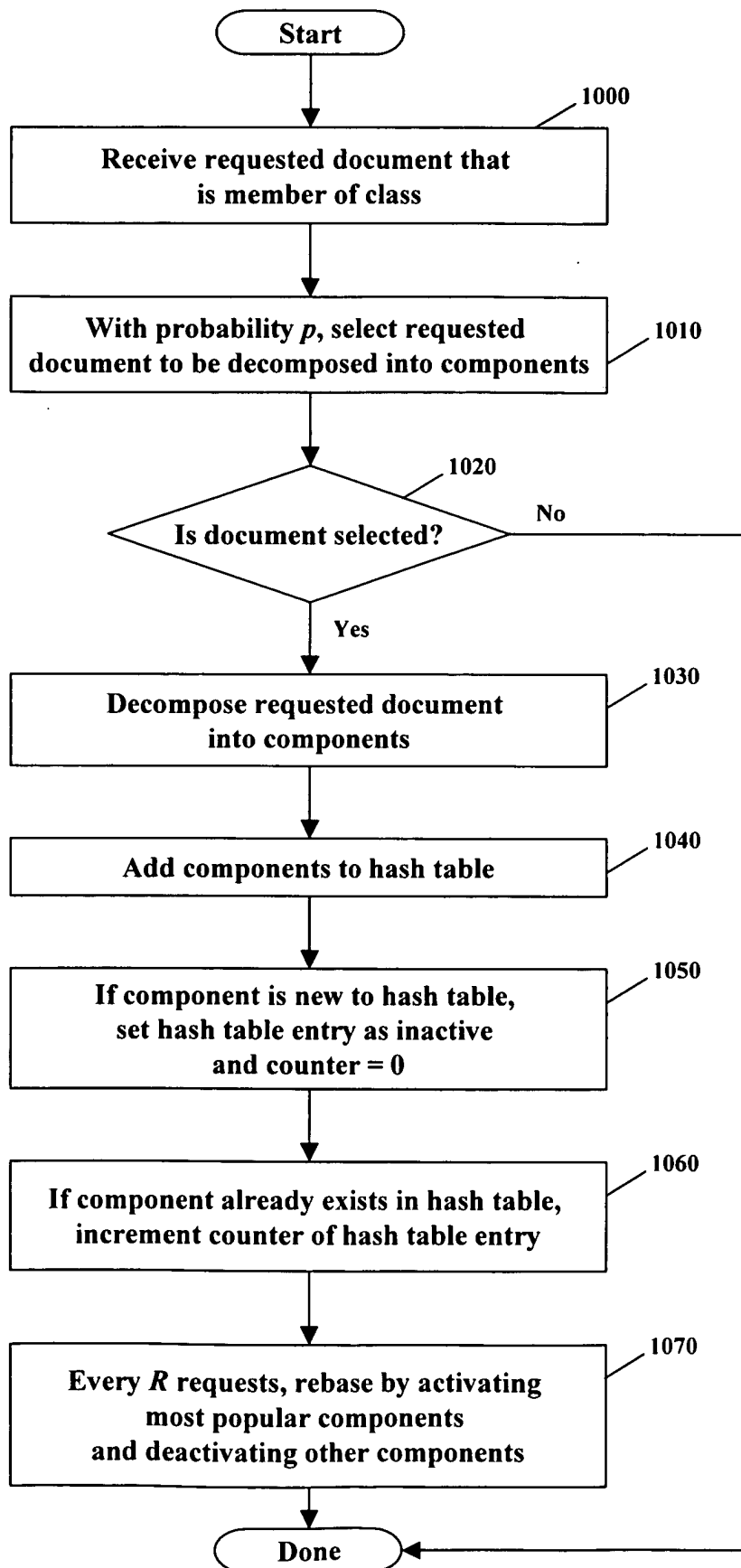


FIGURE 10

402220-2025F00

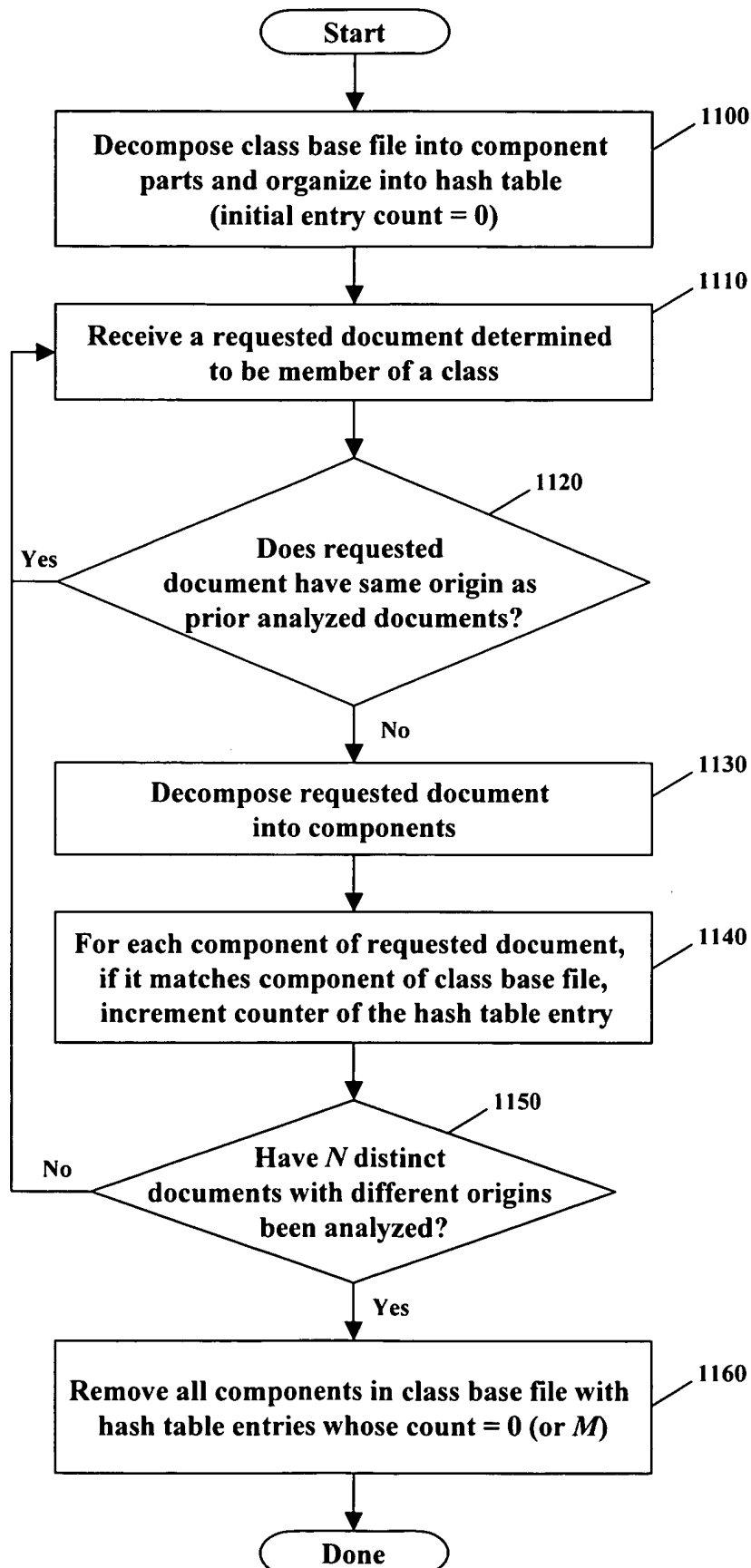


FIGURE 11